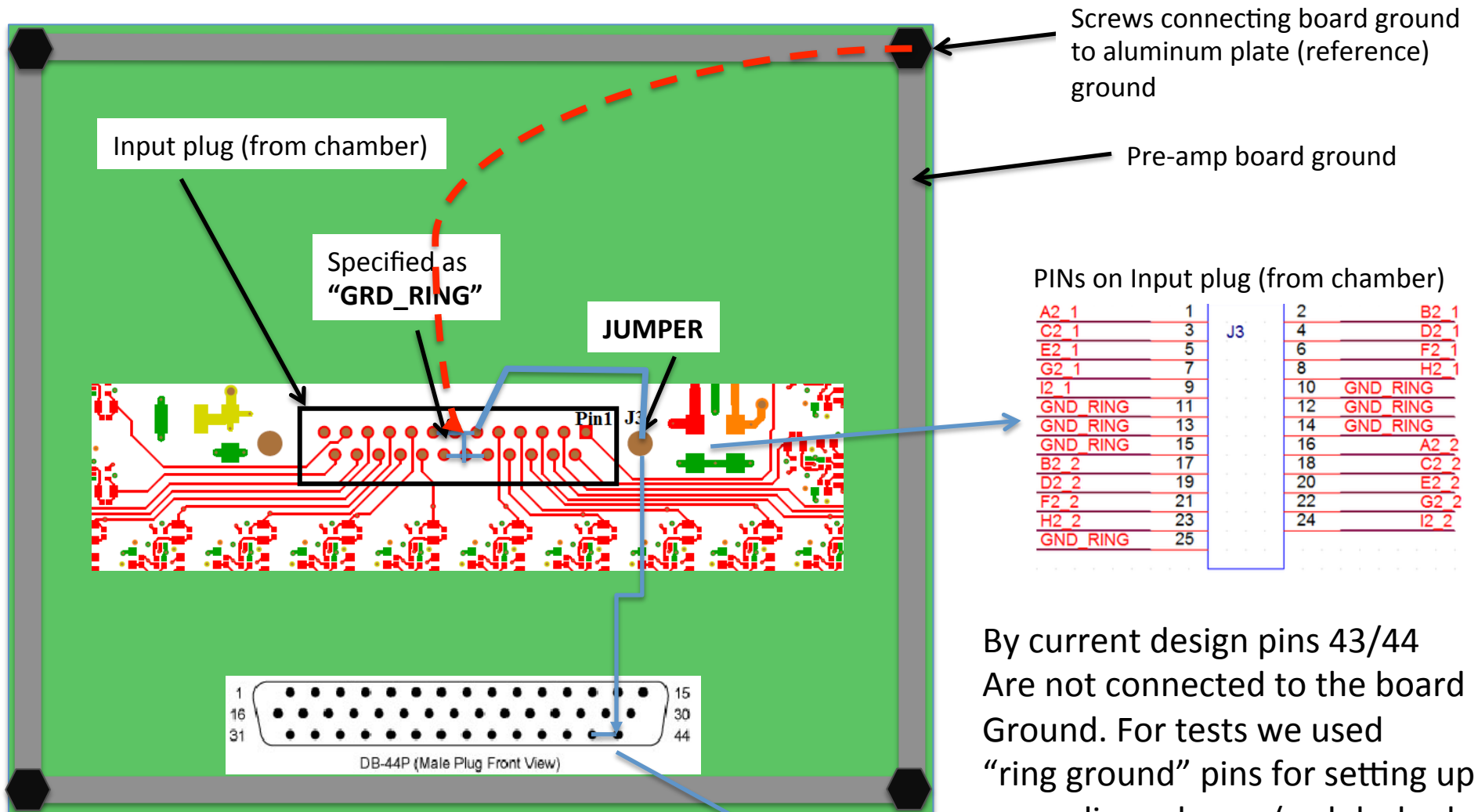




PINs on Input plug (from chamber)

A2 1	1	J3	2	B2 1
C2 1	3		4	D2 1
E2 1	5		6	F2 1
G2 1	7		8	H2 1
I2 1	9		10	GND RING
GND RING	11		12	GND RING
GND RING	13		14	GND RING
GND RING	15		16	A2 2
B2 2	17		18	C2 2
D2 2	19		20	E2 2
F2 2	21		22	G2 2
H2 2	23		24	I2 2
GND RING	25			

By current design pins 43/44
Are not connected to the board
Ground. For tests we used
"ring ground" pins for setting up
grounding scheme (red dashed
line) which won't
be available

1	A2 1	Out+
2	A2 1	Out-
3	B2 1	Out+
4	B2 1	Out-
5	C2 1	Out+
6	C2 1	Out-
7	D2 1	Out+
8	D2 1	Out-
9	E2 1	Out+
10	E2 1	Out-
11	F2 1	Out+
12	F2 1	Out-
13	G2 1	Out+
14	G2 1	Out-
15	H2 1	Out+
16	H2 1	Out-
17	I2 1	Out+
18	I2 1	Out-
19	A2 2	Out+
20	A2 2	Out-
21	B2 2	Out+
22	B2 2	Out-
23	C2 2	Out+
24	C2 2	Out-
25	D2 2	Out+
26	D2 2	Out-
27	E2 2	Out+
28	E2 2	Out-
29	F2 2	Out+
30	F2 2	Out-
31	G2 2	Out+
32	G2 2	Out-
33	H2 2	Out+
34	H2 2	Out-
35	I2 2	Out+
36	I2 2	Out-
37		
38		
39		
40		
41		
42		
43	GND_ADC	
44	GND_ADC	

Pin 43/44 is connected via HDCL-to-VHDCI
cable to individual ADC module ground