Feb. 19 Chris, Jack, Kabir, Mark, Paul, me, Wednesday, February 19, 2014 Carlos, Seppo, David., Irakli, - Carlos - data analysis - coming in the Summer/fall to ORNL. - Seppo - summary. PRAC - each person prepares slides
Michael present them.
Theme: convincing that we are going well.
Threshold: test wire chamber wire chamber, preamps OK (not on critical path).
 David-tone: go through topics identify talking-points - Integration - Jack has a designer - free, full-time - must design alignment pieces. - will have model of beamline / expt for PRAC. - alignment pieces - Jack talk with Josh. - Josh should ideas -> designers implement. - Magnetic coil - HAVE to converge on design of field placement, # windings, fieldmap. - RFSF - 20% offin calculation of inductance - audio amp hasn't arrived. - sevel shipping into - alignment marks

- Wire Chamber - parts in for assempty stand - epoxy to fix leak - readout boards - Advance Circuits: 3-5 days. - Advanced Assembly (waiting on quotes) - Michael has to generate PO's ahead of time. - FOR PRAC: - Mar 7: PCB arrives. - Frame stack built on table. - test HV breakdown w/ 4He. - April: In-beam testing. parameters: a) largest signal Voltage. → new R. 5) plateeu voltage. I indep of V. - Preamps - measuring false asymmetries: -1.5 ± 1.1 × 109 from 1/2hr of data -> 1×10<sup>-10</sup> goal. - working on getting 56 kHz sample clock Irakli & Kabir unsuccessful getting output clock. - construction • One board w/ good noise performance w/40pF capacitance still thermal noise -old measurement was not valid (wrong ground). · 2 weeks to make boards

- DAQ

- Collimation LizPO3 =, lithium phosphete - no abs. of