

2013-11-27

Wednesday, November 27, 2013
8:09 AM

* Irakli

- ISR / OSR done by oversampling.
- based on manual: remembers 4 samples
- ADC knows how many samples to average.
- 10 kHz ISR
 - DAQ crashed w/ Kabir laptop.
 - crashed at higher rate: note 32ch:
50 kHz $\rightarrow$ 51.2 Mbit/s pushing data rate.
- NACC = ISR/OSR accumulator function.

- ## * Kabir
- 10 kHz noise matches Johnson noise on preamp.
- 2 measurements w/ preamp.
 - 1 w/o input, 1 w/ 9.5 V battery & 3.5 M Ω resistor current source.
 - measured each input pin
 - OSR = ISR = 10 kHz
650000 samples in 65s each channel.
 - calculated noise for no input
10 μ V Johnson noise from gain resistor in preamp.
2 M Ω gain resistor
 - experimental noise: 10 or 12 μ V
3.5 M Ω : 0.05V $\rightarrow$ 50 mV
bare ADC: ? (will email)
 - Esti measured bare ADC @ 10 kHz,
averaged 4 successive samples
 $\rightarrow$ noise decreased by a factor of 2.

* David

- 400 μ s fine-grain enough to resolve
- preamp: 4 μ s rise time
want to go to 400 μ s by boxcar
- boxcar inherent in ADC design
 - got from reading literature.
- looked for auto correlation $\rightarrow$ δ -function.
- one other thing: look at rise-time of preamp.
 - measure spectral response.
 - run at 10kHz $\frac{1}{2} \rightarrow$ 128 kHz.

run with/w/o preamp.

- need to beat down pickup
 - RMS value much higher w/ current source.

* Issues:

- triggering on 10 MHz clock? $\rightarrow$ continuous readout.
- synchronizing clock? $\checkmark$
- trigger + N-samp $\checkmark$
- readout 36 channels + 1 $\rightarrow$
- boxcar firmware. $\rightarrow$ ISR/OSR
- digital out bit?

* Jitter: 10 MHz clock

- running at 10 kHz $\rightarrow$ 100ms jitter?
- 128 kHz $\rightarrow$ 8 μ s

$$10 \text{ V}/16 \text{ ms} \times 8 \mu\text{s} = 5 \text{ mV}$$

* New ACQ 435 ELF

TI ADC - same chip?

* Kabir : 4 modes ,

- tested each
- has script with all parameters for 1 mode
- figuring out parameters for 3 other modes.