

2014-04-23

Wednesday, April 23, 2014
12:03 PM

* talk by Kabir, Irakli, Vince.

- clocks:

d0 - master clock 32MHz (binary sampling freq.?)
- never changes (after activation)

d1, d2 2x A62435 modules.

- p7: "mb_clk value1 value 2"

$$51200 \times \frac{64000}{64000} = 51.2 \text{ kHz}$$

Peter says: 49000, 5120 \rightarrow 10 kHz

- cross talk not an issue (I think).

- have a preamp, need cables to test DAQ.

* We also have our isolation transformer

* Need RAID array.

* USB - parallel converter won't work
- need extra pins!

* False asym tests:

⇒ ACTION: get cables → false asym tests.
→ ask Peter for firmware
→ start programming data filter.