David Blythe, Vince, Kabir

* new system
   + Spartan III → Zync
   + 40 ch/box
   + Drawbacks : front panels different.
   + mini VHDI = SCSI5   not SCSI3 (HD68)   both 68 pins
         used in NI systems
         32 ch + 8 ch cards.
   + 4 separate boxes w/ synchronization bus.
         ADC's share 2 clocks : clock & index
         HDMI cable from box → box.
   + FSF, Gating, Sync. need development
         test with continuous streaming mode.

   + VINCE:
         downstream analysis identical
         just extra jitter in the trigger
         200 ns relevant timescale jitter.
         5 boxes : before May. possible end of March.
         want to respin a few more features — 1 month delay
         final firmware : end of May.

   + VINCE: communication?
         OK if others contact? OK.

   + VINCE: local engineering group
         debugging: possibility for on-site assistance?